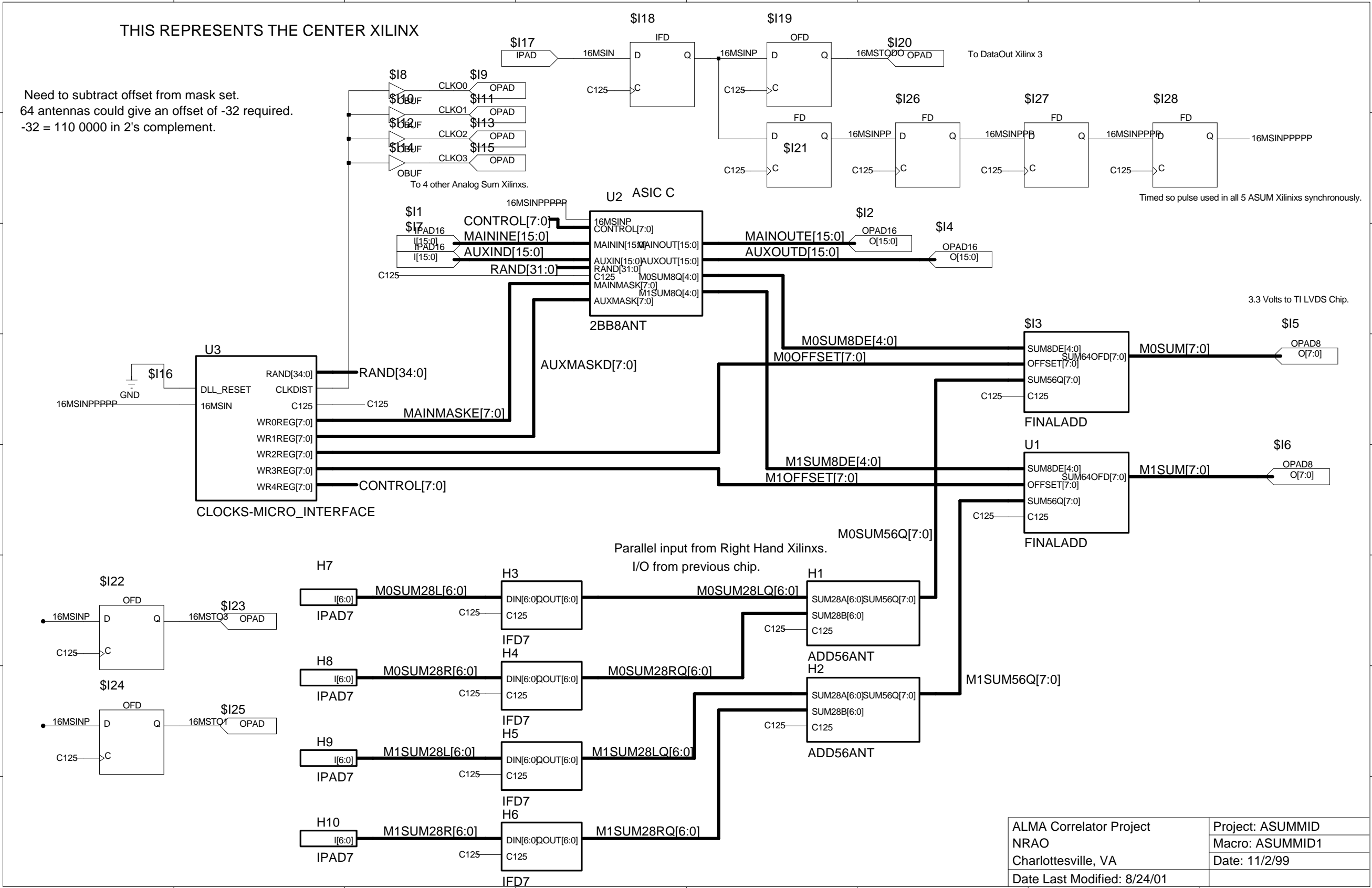


THIS REPRESENTS THE CENTER XILINX

Need to subtract offset from mask set.  
64 antennas could give an offset of -32 required.  
-32 = 110 0000 in 2's complement.



3.3 Volts to TI LVDS Chip.

Parallel input from Right Hand Xilinx.  
I/O from previous chip.

ALMA Correlator Project	Project: ASUMMID
NRAO	Macro: ASUMMID1
Charlottesville, VA	Date: 11/2/99
Date Last Modified: 8/24/01	