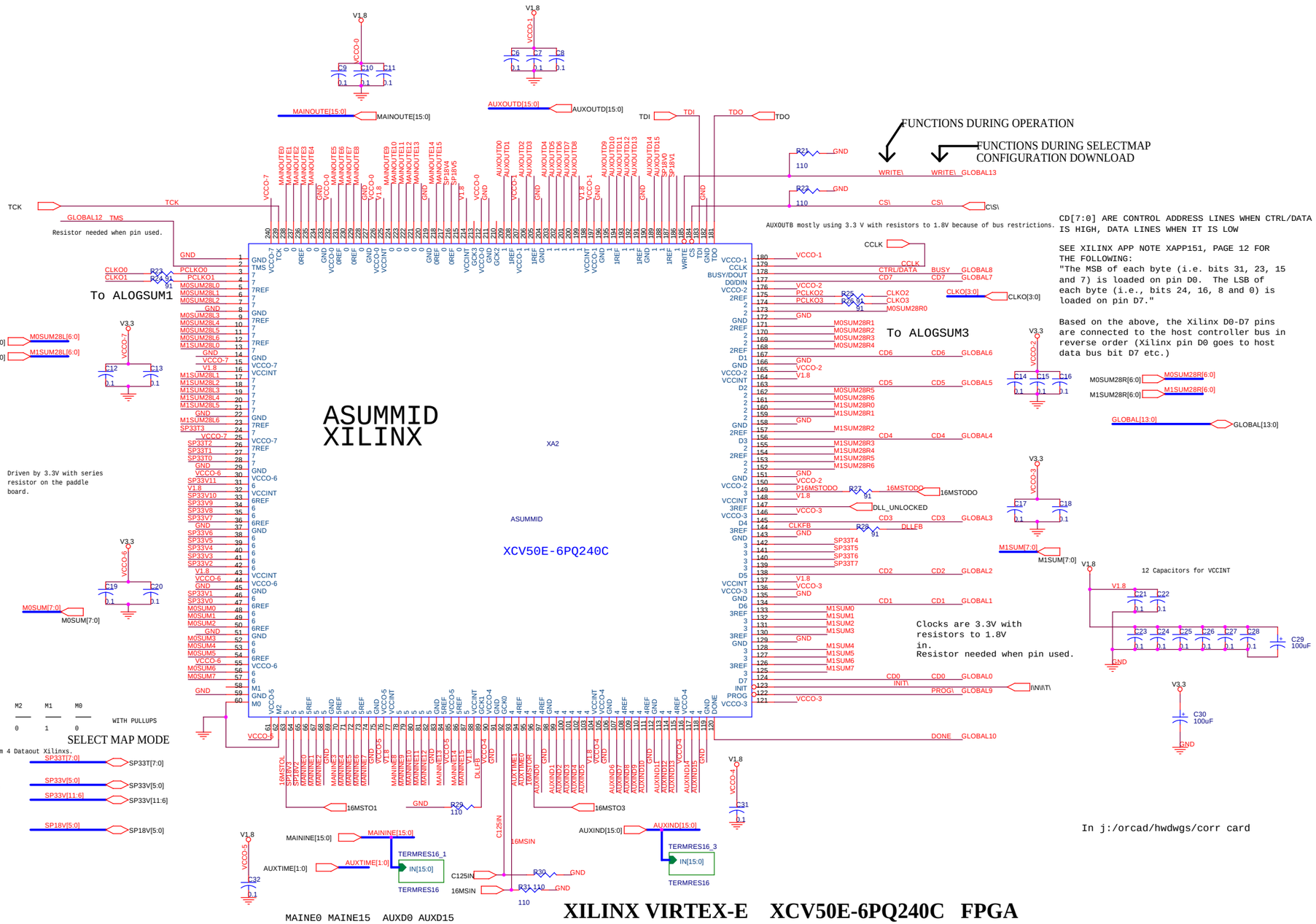
MIRROR IMAGED SINCE ON BACK OF CARD

MAINE0 MAINE15 AUXD0 AUXD15

Edit part, then select the pins and edit properties to change to inputs or outputs.



MAINE0 MAINE15 AUXD0 AUXD15

ALOGSUM0 ALOGSUM1 ALOGSUM2 ALOGSUM3 ALOGSUM4

XILINX VIRTEX-E XCV50E-6PQ240C FPGA

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