



CABLE WAFER PIN ASSIGNMENTS MADE SO AS TO BE COMPATIBLE WITH THE AMP FIELD INSTALLABLE CONNECTORS (6x2 and 6x4 SIZES). THEY DO NOT STACK WITHOUT A LOSS OF ONE CONTACT COLUMN.

SPRPNSS0 R4 110 GND  
SPRPNSS1 R4 110 GND  
SPRPNSS2 R4 110 GND

Space Pins

Not used by Auxiliary time input framing 14 ns pulse current design.

ALXTIMED R5 GND  
ALXTIMC1 R5 GND  
ALXTIMC2 R5 GND

SP1PNS0 R7 110 GND  
SP1PNS1 R7 110 GND  
SP1PNS2 R7 110 GND

Space Pins

TEST POINT

Jumper to JTAG3 if no upper row of ASICs.

To pins increase wait later.

AS1M30E0-3 to test point connector, 4-7 to pins.

To pins increase wait later.

Basic Program Word Strobe

Test points 0-19 out of tops of Dataout Xilinx.

SURFACE MOUNT CAPS AND RESISTORS = 0805 PACKAGES UNLESS OTHERWISE NOTED.

0805 = 80 MILS LONG BY 50 MILS WIDE  
(2.0 MM LONG BY 1.25 MM WIDE)

1% RESISTORS = PARAGONIC BRN502X70V  
4.7k 472 FOR 4.7k  
xxx= 750 FOR 75 OHM

X7E 0.1 uF CAPS = PARAGONIC 80J2V16C104E

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