



- A = PROGWRD[5:0]**
- | Bit Number | 32 SEQUENCER INSTRUCTIONS  |
|------------|--|
| 6          | <b>HOLD</b> - Wait for MSSTB to reset the Program Counter.   |
| 7          | <b>JUMPUP</b> - Jump to uP Address.  |
| 8          | <b>JUMPSEQ</b> - Jump to address A.(2 msbs from uP).   |
| 9(A)       | <b>LDINTCTR</b> - Load Intersection counter with value in A. 2 msbs 10   |
| 10(A)      | <b>LOOPINT</b> - Loop back on Intersection counter, Jumping to A, unless TC. Intersection Counter incremented.   |
| 11(A)      | <b>PAUSE</b> - Load Result Counter with value in A. 2 msbs 11. Loop back on Result counter, freezing the PC, unless TC. Result Counter incremented. Follow by NOOP |
| 12(A)      | <b>LDSELCCTR</b> - Load Select counter with value in A. 2 msbs 11.   |
| 13(A)      | <b>LOOPSEL</b> - Increment Select Counter, Jump to A, unless TC.   |
| 14         | <b>RDCLKENBL</b> - During a PAUSE issue RDCLKENBL  |
| 15(A)      | <b>JUMPFER</b> - If transfer, jump to A. 2 msbs determined by uP.  |

ALMA Correlator Project	Project: DATAOUT
NRAO	Macro: SEQUENCER
Charlottesville, VA	Date: 3/2/00
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See j:\Orca\d\hw\docs\Documentation\dopgm.xls for detailed microcode.  
 See j:\Orca\d\hw\docs\Documentation\corrcard.doc for explanation.