

j:/orcad/hwdwgs/chip/alma1spec.xls Sheet 2 ALMA07004NX0001
ALMA CHIP SERIAL PROGRAM WORD

Spares are distributed to facilitate programming.

<u>BIT NUMBERS</u>	<u>SIGNAL NAMES</u>	<u># bits</u>	<u>FIGURE</u>	<u>DESCRIPTION</u>
15 : 0	R0-M[15:0]	16	2, 4	program row 0 correlator multiplexers
31 : 16	R1-M[15:0]	16	2, 4	program row 1 correlator multiplexers
47 : 32	R2-M[15:0]	16	2, 4	program row 2 correlator multiplexers
63 : 48	R3-M[15:0]	16	2, 4	program row 3 correlator multiplexers
79 : 64	WRAPBLK[15:0]	16	2, 4	In Blk x, select data from previous 256 lag block. (0)
82 : 80	R0-W[2:0]	3	2, 4	program row 0 correlator wrap multiplexers
85 : 83	R1-W[2:0]	3	2, 4	program row 1 correlator wrap multiplexers
87 : 86	Spare Bits	2		
90 : 88	R2-W[2:0]	3	2, 4	program row 2 correlator wrap multiplexers
93 : 91	R3-W[2:0]	3	2, 4	program row 3 correlator wrap multiplexers
95 : 94	Spare Bits	2		
96 : 96	AUXEN	1	3	Enable data activity out of the Aux Bus outputs.
97 : 97	LTOR	1	3	Program right side output bus drivers.
99 : 98	CENTERBUS[1:0]	2	3	program internal tri-state drivers
100 : 100	LEADUR	1	4	Leads in the Upper Right
101 : 101	LEADLL	1	4	Leads in the Lower Left
103 : 102	Spare Bits	2		
108 : 104	SELPLY[4:0]	5	3, 8	program delay lines in DB0 to DB15 and blanking
111 : 109	Spare Bits	3		
115 : 112	FULLACC[3:0]	4	6	rows 3 thru 0, 21 bit/25 bit accumulator bit
119 : 116	OVERSAMP[3:0]	4	5	rows 3 thru 0 oversample program bit
120 : 120	CKPINEN	1	1	Enable C125 Out to conserve power.(0)
121 : 121	SETOUT	1	none	Set Result Outputs for test (0)
122 : 122	Spare Bit	1	none	(Was RESETOUT)
123 : 123	Spare Bit	1		(Was ALTBLNK)
124 : 124	RESETENB	1	8	Inhibit Dump to Storage (0)
125 : 125	RC_TSTE64	1	6	0 for 16 bit counter. 1 for 2, 8 bit counters.(0)
127 : 126	Spare Bits	2		
TOTAL		128		Value for normal operation shown above in ().

LSB of an array always assigned to the lowest bit numbers.

Revisi 4/10/00 Add ALTERNATEBLANK

7/21/00 Add RESETOUT

10/5/00 Reorder bits to put on convenient byte boundaries.

10/5/00 Change RESETEN to RESETENB which is of the opposite sense.